Doc. No.:



PRODUCT SPECIFICATION

MONO LCD MODULE MODEL: C0802A0SBY6B-A0 Ver:1.0

< >> Preliminary Specification

< ♦> Finally Specification

CUSTOMER'S APPROVAL						
CUSTOMER:						
SIGNATURE: DATE:						

APPROVED	PM	PD	PREPARED
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Doc. No.:

Revision Status

Version	Revise Date	Page	Content	Modified By
VER 1.0	2011.04.12		First Issued	

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1. Features

The features of LCD are showed as follows

* Display mode : STN/Yellow-Green/Transflective/Positive

* Controller IC : AIP31066(English-Japanese)

* Display format : 8X2Characters

* Interface Input Data : 4 bit or 8 bit MPU

* Driving Method : 1/16Duty, 1/4Bias

* Viewing Direction : 6 O'clock

* Backlight : LED Unit /Yellow-Green (bottom)

*Sample NO. : EC0802A0SBY6B-A0_01/20110409

2. MECHANICAL SPECIFICATIONS

Item	Specification	Unit
Module Size	58(W) x32(H) x13.3MAX(T)	mm
Viewing Area	38(W) x 16(H)	mm
Activity Display Area	27.81(W)x11.50(H)	mm
Character Font	5x8 Dots	-
Character Size	2.96(W)x5.56(H)	mm
Character Pitch	3.55(W)x5.94(H)	mm
Dot Size	0.56(W)x0.66(H)	mm

3. ELECTRICAL SPECIFICATIONS

3-1 ABSOLUTE MAXIMUM RATINGS (Ta = 25 °C)

Item	Symbol	Min	Max	Unit
Supply Voltage For Logic	Vdd	0.3	7.0	٧
Supply Voltage For LCD Drive	V_{LCD}	V _{DD-} 15	VDD+0.3	V
Input Voltage	Vin	-0.3	VDD+0.3	V
Operating Temp.	Тор	-20	+70	°C
Storage Temp.	Tst	-30	+80	°C

^{*.} NOTE: The response time will be extremely slow when the operating temperature is around -10°C, and the back ground will become darker at high temperature operating.

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3-2 ELECTICAL CHARACTERISTICS

ltem		Symbol	Test Condition	Min.	Тур.	Max.	Unit
Logic supply Voltage		VDD – Vss		4.5	5	5.5	\
LCD Drive		V_{OP}	Ta = 25 °C	4.4	4.7	5.0	V
Input Voltage	"H" Level	V _{IH}	V _{DD} =5V ± 10%	2.2	-	VDD	V
input voltage	"L" Level	V _{IL}		-0.3	-	0.6	V
Frame Frequency		f_{FLM}		ı	-	84.4	Hz
Current Cons	umption	I _{DD}		-	1.57	-	mA

3-3 BACKLIGHT

3-3-1. Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Forward Current	IF		-	-	70	mA
Reverse Voltage	VR	Ta = 25 °C	-		10	V
Power Dissipation	PD		-	-	300	mW

3-3-2. Electrical-optical Characteristics

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Forward Voltage	VF		4.0	4.1	4.2	V
Average Luminous Intensity	lv	If=70mA Ta = 25 °C	150	-	-	cd/m ²
Peak wave length	λр		570	572	576	nm

The brightness is measured without LCD panel

For operation above 25 °C,The lfm & Pd must be derated , the current derating is -0.36mA/ °C for DC drive and -0.86mA/ °C for Pulse drive ,the Power dissipation is -0.75mW/ °C.The product working current must not more than the 60% of the lfm or lfp according to the working temperature.

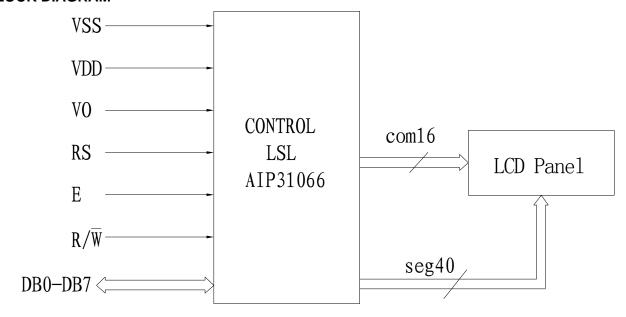
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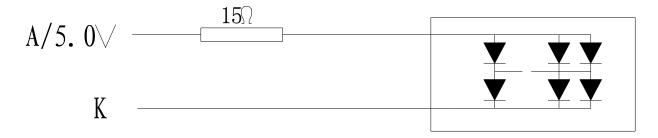
4. TERMINAL FUNCTIONS AND BLOCK DIAGRAM

4-1 INTERFACE PIN FUNCTION DESCRIPTION

PIN NO.	SYMBOL	FUNCIONS
1	VSS	Ground
2	VDD	Supply voltage for logical circuit
3	V0	Supply voltage for LCD driving
4	RS	A signal for selecting registers. 1: Data Register (for read and write) 0: Instruction Register (for write)
5	R/W	A signal for selecting read or write actions.1: Read, 0: Write.
6	E	A enable signal for reading or writing data.
7-14	DB0~DB7	8 Bit Data Bus
15	Α	Backlight (5.0V)
16	K	Backlight (-)

4-2 BLOCK DIAGRAM





Backlight

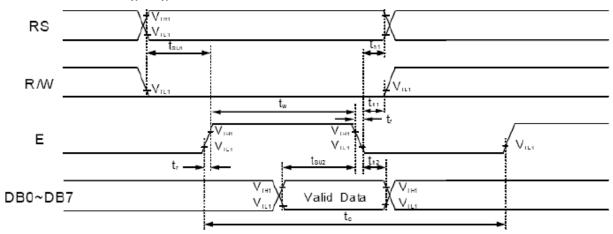
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5. TIMING CHARACTERISTICS

5 - 1 Write mode

Mode	Characteristic	Symbol	Min.	Тур.	Max.	Unit
	E Cycle Time	tc	500	-	-	
	E Rise / Fall Time	t _R ,t _F	-	-	20	
Write Mode (Refer to Fig-1)	E Pulse Width (High, Low)	t _w	230	-	-	
	R/W and RS Setup Time	t _{su1}	40	-	-	ns
	R/W and RS Hold Time	t _{H1}	10	-	-	
	Data Setup Time	t _{su2}	80	-	-	
	Data Hold Time	t _{H2}	10	-	-	

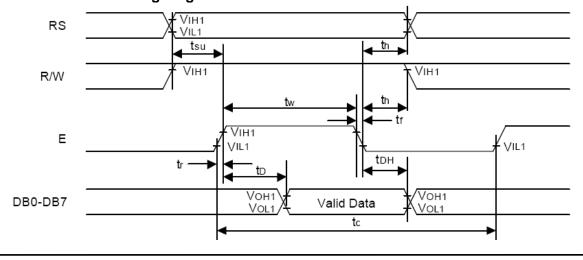
5-2 Write mode timing diagram



5.3 Read mode

, 3.3 Nead IIIOGE	1					
	E Cycle Time	t _c	500	-	-	
	E Rise / Fall Time	t _R , t _F	-	-	20	
Read Mode (Refer to Fig-2)	E Pulse Width (High, Low)	t _w	230	-	-	
	R/W and RS Setup Time	t _{su}	40	-	-	ns
	R/W and RS Hold Time	t _H	10	-	-	
	Data Output Delay Time	t _D	-	-	120	
	Data Hold Time	t _{DH}	5	-	-	

5-4Read mode timing diagram



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6. COMMAND LIST

Instruction				Inst	ructi	on C	ode				Description	Execution time (fosc=
instruction	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Description	270 kHz)
Clear Display	0	0	0	0	0	0	0	0	0	1	Write "20H" to DDRAM and set DDRAM address to "00H" from AC	1.53 ms
Return Home	0	0	0	0	0	0	0	0	1	-	Set DDRAM address to '00H' from AC and return cursor to its original position if shifted. The contents of DDRAM are not changed.	1.53 ms
Entry Mode Set	0	0	0	0	0	0	0	1	I/D	SH	Assign cursor moving direction and enable the shift of entire display.	39 μs
Display ON/ OFF Control	0	0	0	0	0	0	1	D	С	В	Set display(D), cursor(C), and blinking of cursor(B) on/off control bit.	39 µs
Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	-	-	Set cursor moving and display shift control bit, and the direction, without changing of DDRAM data.	39 μs
Function Set	0	0	0	0	1	DL	N	F	-	-	Set interface data length (DL: 8-bit/4-bit), numbers of display line (N: 2-line/1-line) and, display font type (F:5×11dots/5×8 dots)	39 μs
Set CGRAM Address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0	Set CGRAM address in address counter.	39 μs
Set DDRAM Address	0	0	1	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Set DDRAM address in address counter.	39 µs
Read Busy Flag and Address	0	1	BF	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.	0 µs
Write Data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM (DDRAM/CGRAM).	43 µs
Read Data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM (DDRAM/CGRAM).	43 μs

* "-": dont care

NOTE: When an MPU program with checking the Busy Flag(DB7) is made, it must be necessary 1/2Fosc is necessary for executing the next instruction by the falling edge of the 'E' signal after the Busy Flag (DB7) goes to "Low".

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7. CHARACTER GENERATOR ROM

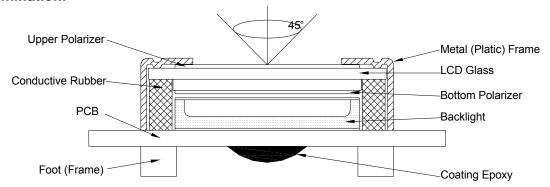
Lever Bits	00 00	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	11 00	1 101	11 10	1111
xxxx0000	RAM (1)			0	9	P	•	F				_	9	Ę	CC	þ
xxxx0001	(2)		!	1	A	Q	a	9			•	7	Ŧ	4	ä	q
xxxx0010	(3)		Ш	2	В	R	b	r			Γ	1	ŋ	X	ß	8
xxxx0011	(4)		#	3	C	5	C	s			L	Ż	Ť	ŧ	ε	80
xxxx0100	(5)		\$	4	D	Ţ	d	ŧ.			`	I	ŀ	þ	μ	Ω
xxxx0101	(6)		7	5	E	U	e	u			•	7	†	1	σ	ü
xxxx0110	(7)		&	6	F	Ų	f	Ų			7	Ħ	₹	3	ρ	Σ
xxxx0111	(8)		7	7	G	W	9	W			7	‡	Z	Ŧ	9	π
xxxx1000	(1)		(8	H	X	h	X			4	7	*	IJ	Ţ	X
xxxx1001	(2))	9	Ι	Y	i	y			Ċ	ን	Į	ιb	-1	Ч
xxxx1010	(3)		*	•	J	Z	j	Z			I	J	n	ŀ	j	Ŧ
xxxx1011	(4)		+	;	K		k	{			#	Ħ	E		×	F
xxxx1100	(5)		7	<	L	¥	1				t	Ð	7	7	¢	Ħ
xxxx1101	(6)		_	=	M]	M	}			1	Z	^	_,	Ł	÷
xxxx1110	(7)		•	>	H	٨	n	+			3	t	#		ħ	
xxxx1111	(8)		/	?	0	_	0	+			'n	y	7	•	Ö	

Note: The user can specify any pattern for character-generator RAM.

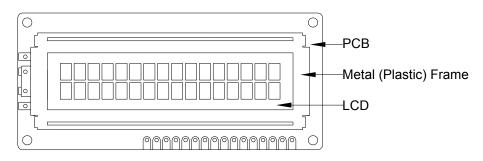
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8. QUALITY SPECIFICATIONS

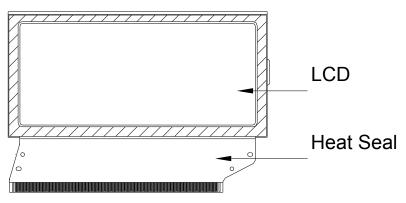
- 8 1. LCM Appearance and Electric inspection Condition
- 1. Inspection will be done by placing LCM 30cm away from inspector's eyeballs under normal illumination.



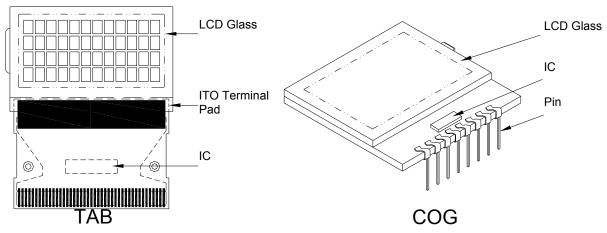
- 2. View Angle: with in 45° around perpendicular line.
- 8-2. Definition
- 1. COB



2. Heat Seal



3. TAB and COG



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8-3. Sampling Plan and Acceptance

1.Sampling Plan

MIL - STD - 105E (\parallel) ordinary single inspection is used.

2.Acceptance

Major defect: AQL = 0.65%Minor defect: AQL = 1.5%

8-4. Criteria

1.COB

Defect	Inspection Item	Inspection Standards	
Major	PCB copper flakes peeling off	Any copper flake in viewing Area should be greater than 1.0mm ²	Reject
Major	Height of coating epoxy	Exceed the dimension of drawing	Reject
Major	Void or hole of coating epoxy	Expose bonding wire or IC	Reject
Major	PCB cutting defect	Exceed the dimension of drawing	Reject

2.SMT

Defect	Inspection Item	Inspection Standa	ards
Minor	Component marking not readable		Reject
Minor	Component height	Exceed the dimension Of drawing	Reject
Major	Component solder defect (missing, extra, wrong component or wrong orientation		Reject
Minor	Component position shift component soldering pad	X < 3/4Z Y > 1/3D	Reject
Minor	Component tilt component soldering pad	Y > 1/3D	Reject
Minor	Insufficient solder component PAD	θ <u><</u> 20°	Reject

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3. Metal (Plastic) Frame

Defect	Inspection Item	lı	nspection Standa	rds		
Major	Crack / breakage	Any	/where	Reject		
		W	L	Acceptable of Scratch		
		w<0.1mm	Any	Ignore		
		0.1 <u><</u> w<0.2mm	L <u><</u> 5.0mm	2		
Minor	Frame Scratch	0.2 <u><</u> w<0.3mm	L <u><</u> 3.0mm	1		
l		w <u>></u> 0.3mm	Any	0		
		Note: 1. Above criteria applicable to scratch lines with distance greater than 5mm. 2. Scratch on the back side of frame (no visible) can be ignored.				
				Acceptable of Dents / Pricks		
		Φ<	2			
	Frame Dent , Prick	1.0<4	1			
Minor	$=\frac{L+W}{2}$	1.51	0			
	2	Note: 1. Above criteria applicable to any two dents / pricks with distance greater than 5mm 2. Dent / prick on the back side of frame (not visible) can be ignored				
Minor	Frame Deformation	Exceed the dimension of drawing				
Minor	Metal Frame Oxidation		Any rust			

4. Flexible Film Connector (FFC)

4. Flexible F Defect		ection Item	Inspection Standards				
Minor	Tilted soldering		Within the angle +5°	Acceptable			
Minor	Uneven s	older joint /bump		Reject			
			Expose the conductive line	Reject			
Minor	Hole	= \frac{L + W}{2}	Φ > 1.0mm	Reject			
Minor	Y-1	sition shift	Y > 1/3D	Reject			
	- X - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1		X > 1/2Z	Reject			

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5. Screw

Defect Inspection Item		Inspection Standards	
Major	Screw missing/loosen		Reject
Minor	Screw oxidation	Any rust	Reject
Minor	Screw deformation	Difficult to accept screw driver	Reject

6. Heatseal 、TCP 、FPC

Defect	Inspection Item	Inspection Standards	
Major	Scratch expose conductive layer		Reject
Minor	HS Hole $= \frac{L + W}{2}$	Φ> 0.5mm	Reject
Major	Adhesion strength	Less than the specification	Reject
Minor	Position shift	Y > 1/3D	Reject
Milnor	X	X > 1/2Z	Reject
Major	Conductive line break		Reject

7. LED Backing Protective Film and Others

Defect	Inspection Item	Inspection Standards				
		Acceptable number of units				
		Ф <u><</u> 0.10mm	ignore			
		0.10<Φ <u><</u> 0.15mm	2			
Minor	LED dirty, prick	0.15<Ф <u><</u> 0.2mm	1			
		Φ>0.2mm	0			
		The distance between any two spots should be ≥ Any spot/dot/void outside of viewing area is acce				
Minor	Protective film tilt	Not fully cover LCD Re				
Major	COG coating	Not fully cover ITO circuit	Reject			

8. Electric Inspection

Defect Inspection Item		Inspection Standards	
Major	Short		Reject
Major	Open		Reject

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9. Inspection Specification of LCD

Defect	ction Specific Insp	ect Item			Ins	spection	St	andards	5	
	•		W			0.03		0.0 <u><</u> 0.0		V>0.05
		* Glass Scratch	L		L	<5		L<3		Any
Minor	Linear Defect	Polarizer ScratchFiber and Linear	ACC. NO.	1			1		Reject	
		material	Note	L is the length and W is the width of the defect					efect	
		* Foreign material	Φ	Φ < 0.		0.1<⊕ <u><</u> 0	.15 ().15<⊕ <u><</u> 0	.2	Φ>0.2
	Black Spot and	between glass and polarizer or glass		3EA . 100mr	/ m²	2		1		0
Minor	Polarizer Pricked	and glass * Polarizer hole or protuberance by external force	Note	Φ is the average diameter of the defect. Distance between two defects > 10mm.						
		* Unobvious	Φ	q	⊅ <u><</u> (0.3	0.3	<Ф <u><</u> 0.5	0.	5 <⊕
	White Spot	transparent foreign material between	ACC. NO.	3EA	/ 10	00mm ²		1		0
Minor	and Bubble in polarizer	glass and glass or glass and polarizer * Air protuberance between polarizer and glass	Note		Φ is the average diameter of the defect. Distance between two defects > 10mm.					
			Φ	Φ <u><</u> 0.1	0	0.10<Φ <u><</u> 0		0.20<⊕ <u><</u> 0.25		Φ>0.25
		w		3EA / 100mm	BEA / 2		1		0	
Minor	Segment Defect	-W-		W is mo	ore	than 1/2 s	egme	nt width		Reject
	20.000	W	Note	Note $= \frac{L + W}{2}$ Distance between two defect is 10			ect is 10m	m		
			Φ	Φ <u><</u> 0.1	<0.10 0.10<⊕<0		<0.20 0.20<⊕<0.25		<u><</u> 0.25	Ф>0.25
	Protuberant	w W	W	Glue W <u><</u> 1/2 S W <u><</u> 0.2				Ignore		
Minor	Segment	= (L + W) / 2	ACC. NO.	3EA / 100mm	/ n ²	2	1		0	
			1. Seg	ment				•		•
			Е	3			0.4 <e< td=""><td>3<u><</u>1.0mm</td><td>B>′</td><td>1.0mm</td></e<>	3 <u><</u> 1.0mm	B>′	1.0mm
Minor	Assembly		B-	Α	B-/	A<1/2B	В-	A<0.2	B-A	N<0.25
	Mis-alignment	B	Jud		Acceptable		Acceptable Acce		eptable	
		- 2 Max	2. Dot Matrix							
			Deformation>2°				Reject			
Minor	Stain on LCD Panel Surface		Accept when stains can be wiped lightly with a soft cloth or a similar one. Otherwise, judged according to the above items: "Black spot" and "White Spot"							

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9. RELIABILITY

NO.	ltem	Condition	Criterion		
1	High Temperature Operating	70℃, 96Hrs			
2	Low Temperature Operating	-20℃, 96Hrs			
3	High Humidity	50℃, 90%RH, 96Hrs			
4	High Temperature Storage	80℃, 96Hrs			
5	Low Temperature Storage	-30℃, 96Hrs	No defect in cosmetic and operational functi		
6		Random wave	on allowable.		
	Vibration	10 ~ 100Hz	Total current Consumption should be below doub		
0	VIDIALIOII	Acceleration: 2g			
		2 Hrs per direction(X,Y,Z)			
		-20℃ to 25℃ to 70℃			
7	Thermal Shock	(60Min) (5Min) (60Min)			
		16Cycles			
		Contract Discharge Voltage: +1 ~ 5kV and –1 ~ –5kV	There will be discharged ten times		
8	ESD Testing	Air Discharge Voltage: +1 ~ 8kV and –1 ~ -8kV	at every discharging voltage cycle. The voltage gap is 1kV.		

Note: 1) Above conditions are suitable for our company standard products.

2) For restrict products, the test conditions listed as above must be revised.

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10. HANDLING PRECAUTION

(1) Mounting Method

The panel of the LCD Module consists of two thin glass plates with polarizers, which easily get damaged since the Module is fixed by utilizing fitting holes in the printed circuit board. Extreme care should be taken when handling the LCD Modules.

(2) Caution of LCD handling & cleaning

When cleaning the display surface, use soft cloth with solvent (recommended below) and wipe lightly.

- Isopropyl alcohol
- Ethyl alcohol
- Tricolor trifler thane

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface. Do not use the following solvent:

- Water
- Kenton
- Aromatics

(3) Caution against static charge

The LCD Module use C-MOS LSI drivers, so we recommend that you connect any unused input terminal to VDD or VSS, do not input any signals before power is turned on. And ground your body, Work/assembly table. And assembly equipment to protect against static electricity.

(4) Packaging

- Modules use LCD elements, and must be treated as such. Avoid intense shock and falls from a height.
- To prevent modules from degradation. Do not operate or store them exposed directly to sunshine or high temperature/humidity.

(5) Caution for operation

- It is indispensable to drive LCD's within the specified voltage limit since the higher voltage than the limit shorten LCD life. An electrochemical reaction due to direct current causes LCD deterioration, Avoid the use of direct current drive.
- Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD's show dark color in them. However those phenomena do not mean malfunction or out of order with LCD's .which will come back in the specified operating temperature range.
- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- -A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.

Usage under the relative condition of 40°C, 50%RH or less is required.

(6) Storage

In the case of storing for a long period of time, (For years) for the purpose or replacement use, the following ways are recommended.

- Storage in a polyethylene bag with sealed so as not to enter fresh air outside in it, And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light is. Keeping temperature in the specified storage temperature range.
- Storing with no touch on polarizer surface by the anything else. (It is recommended to store them as they have been contained in the inner container at the time of delivery)

(7) Safety

- It is recommendable to crash damaged or unnecessary LCD into pieces and wash off liquid crystal by using solvents such as acetone and ethanol. Which should be burned up later. When any liquid crystal leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water.

Issued Date: 2011.04.12 Doc. No.: 11. OUTLINE DIMENSION A М Q \mathcal{C} APPROVED SYMBOL VssVdd R/W DB0DB2 DB3 DB4 DB5 DB6DB7 DB1 Vo RS Щ A \mathbf{M} PIN NO. REVISION RECORD 10 12 13 7 15 16 2 $\boldsymbol{\omega}$ 4 S 9 ∞ 6 First Issue DOT SIZE SCALE 5/1 2011.03.25 mm 0.59 BYCK REV 13.30MAX 2.96 8.70 0.66 **FRONT** 0.56 \Box 0.60 0.70 5.56 0.38 4-ø2.5 PTH 3-Ø5.0 PAD 2.50 27.00 ± 0.3 6-Ø1.8 PAD 6-Ø1.0 PTH \bigcirc 0 0 PIN DETAIL 2.54 5000000 (27.81 A.A.) 58.00 ± 0.5 44.70 ± 0.3 38.00(V.A.) 53.00±0.3 1.70 (P2.54x7 8*T.*71 *1*8.*δ* 00000000 6.65 (10.25)(11.50 A.A.) (15.10)2.50 10.00 16.00(V.A. 8.00 2.80 26.40 ± 0.3 $32.0 \pm\! 0.5$

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